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1030B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Dagobert M. DeLEEuw et al.
SERIAL NO.: 09/817,107 / EXAMINER: Kiesha L. Rose
FILED: March 26, 2001 ART UNIT: 2822
FOR: INTEGRATED CIRCUIT PROVIDED WITH A SUBSTRATE
AND WITH A MEMORY, TRANSPONDER, AND METHOD
OF PROGRAMMING A MEMORY

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL REJECTION

Dear Sir:

In response to the Final Office Action dated July 16, 2003, the Applicant hereby requests amendment of the above-identified application as follows:

RECEIVED
SEP 15 2003
TECHNOLOGY CENTER 2800